

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Shunpei YAMAZAKI et al.)
Serial No. 08/818,884) Art Unit: 2515
Filed: March 17, 1997) Examiner: T. Nguyen
For: ELECTRO-OPTICAL DEVICE AND)
METHOD FOR MANUFACTURING THE)
SAME) Date: August 13, 1998

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INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that the references be made of record in the above identified application. These references include U.S. Patent 5,701,167 and the references cited therein. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Japanese Laid-Open Patent No. 58-27364 teaches to constitute a thin film transistor by a semiamorphous semiconductor.

Japanese Laid-Open Patent No. 1-128534 discloses a COG (chip-on-glass) method for forming a peripheral circuit for a liquid crystal device. Reference numeral 1 shows a glass substrate, 2; light blocking gate electrode, 3; light blocking film, 4; gate insulating film, 5; a-Si:H film, 9; wiring film, 50; height of a bump, 60; light incident direction, 100; TFT region, 200; COG region.

Japanese Laid-Open Patent No. 63-96636 teaches a liquid crystal device having


complementary thin film transistors.

"Color Liquid Crystal Display" teaches a peripheral circuit having a complementary configuration. It also teaches to form a peripheral circuit and active matrix elements through the same process.

Japanese Laid-Open Patent No. 2-251992 teaches to manufacture a peripheral circuit and active matrix elements through the same process.

The Commissioner is hereby authorized to charge fees (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,



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